Remarks

This Response to Office Action is in response to the Office Action, dated September 26, 2003, in which the Examiner rejected claims 1-3 and 9-19 under 35 U.S.C. § 102(e) as being anticipated by U.S. Patent No. 6,445,714 issued to d'Anjou et al. ("d'Anjou"). The Examiner also rejected claims 4-8 under 35 U.S.C. § 103(a) as being unpatentable over D'anjou in view of U.S. Patent No. 6,275,520 issued to Nakamura et al ("Nakamura"). Applicant is submitting the following remarks in response. Claims 1-19 remain pending. Reexamination and Reconsideration in light of the remarks made herein are respectfully requested.

D'anjou describes a code generation apparatus that uses multiple passes to compare all possible shifts of code sequences through a number of shift registers (See d'Anjou, Abstract). To do this, the contents of the shift registers are feed to a state buffer. In fact, d'Anjou states that "multiple passes of the multiple correlation apparatus is required to fully correlate a required range." (See d'Anjou, Col. 9, lines 43-45). This is consistent with the stated purpose in d'Anjou which is to "provide a system and method by which parallel correlators can be implemented more efficiently." (Col. 2, lines 29-31). In particular, it appears that d'Anjou is directed to providing a system of parallel correlators without the need for having a separate code generator for each correlator. (See d'Anjou, Col. 2, lines 10-25).

In contrast, the present invention is directed to a device which generates a desired code phase by combining the outputs of a shift register using a control signal, as recited in the independent claims of the present application. The recited "logic branch" uses the control

signal to combine the various code phases output from the shift register, as also recited in the claims.

Applicant submits that d'Anjou, taken alone or in combination with Nakamura, fails to anticipate or render obvious the claims of the present application. In particular, Applicant submits that the cited references fail to disclose or suggest the use of a "combination control signal," as recited in the claims. In the Office Action, the examiner appears to rely on element **5** of Fig. 2 in d'Anjou for disclosure of the combination control signal. However, element **5** is a buffer. This buffer is used to provide previously stored portions of the received signal for subsequent "passes." As clearly stated in d'Anjou, the "correlators **6**, coupled to the buffers **5**, correlate the "piece" of the received signal stored in the buffers **5** against all of the possible states, over the uncertainty period, of local PN code generators **10**." (See d'Anjou, Col. 4, lines 53-56)(emphasis added). d'Anjou goes on the state that the piece of the received signal stored in the buffers **5** is to be correlated against segments of the local replicas. (Col. 6, line 21-22) The recited I and Q inputs from buffers **5** are not "control signals," as recited in the present claims, but are simply previously stored signal samples (e.g., five-bit samples) that are multiplied by the bits output from the shift registers over a series of passes (e.g., 32 passes in the case of Table 2). As such, Applicant submits that d'Anjou fails to disclose or suggest all of the recited elements in the independent claims of the present invention.

Applicant further submits that the dependent claims are allowable by virtue of depending on allowable base claims.

Conclusion

In view of the remarks made above, it is respectfully submitted that the pending claims are in condition for allowance, and such action is respectfully solicited.

Respectfully submitted,

IRELL & MANELLA LLP

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Kimberley G. Nobles
Reg. No. 38,255

840 Newport Center Drive
Suite 400
Newport Beach, CA 92660
(949) 760-0991

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Non-Fee Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on January 26, 2004.

Darla Cleveland Date: January 26, 2004